Simulation of SEU cross-sections using MRED under conditions of limited device information

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Objective

- Develop an upset response model of a Sandia CMOS6r 16Kb SRAM block using:
  - “Best guess” assumptions about the process and geometry (= naïve model)
  - Direct ionization, low-energy beam test results
- Simulate single-event upset (SEU) cross-sections:
  - Include angular and high-energy responses
  - Compare with beam test data for model validation
Sandia CMOS6r technology

- 0.5 um rad hardened twin-well CMOS technology/process
- Tungsten vias
- Shallow-trench isolation
- 3 metal layers (Al/Cu)

The SRAM block we are simulated has no feedback resistors
Accelerator beam test data

- Extensive set of test data available
- SEU cross-section for a fixed LET depends on ion energy and species

<table>
<thead>
<tr>
<th>Test Facility</th>
<th>Species-Atomic Mass</th>
<th>Energy (MeV)</th>
<th>LET (MeV-cm²/mg)</th>
<th>Angles Tested</th>
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</thead>
<tbody>
<tr>
<td>BNL C-12</td>
<td>12</td>
<td>1.464</td>
<td>0, 30, 45</td>
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<td>BNL C-12</td>
<td>98.7</td>
<td>5.07</td>
<td>0, 45</td>
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<td>BNL F-19</td>
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<td>3.428</td>
<td>0, 45</td>
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<td>BNL Si-28</td>
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<td>8.126</td>
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<td>BNL Ti-48</td>
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<td>11.81</td>
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<td>BNL Ni-58</td>
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<td>BNL Br-81</td>
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<td>38.24</td>
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<td>TAMU Ar-40</td>
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<td>TAMU Kr-84</td>
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<td>TAMU Kr-40</td>
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<td>TAMU Xe-136</td>
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<td>40.2</td>
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</tbody>
</table>
Accelerator beam test data

- Developed MRED model based solely upon normal-incident, low-energy BNL test data
- Defined threshold LET as 7.5 MeV*cm²/mg
  — for upsets induced by direct ionization
MRED model

- Sensitive volume (SV) defines region of charge collection
  – is for a single bit

- SV constructed from concentrically-nested set of 10 regions having different charge collection efficiencies*
  – Regions sized according to cross-section area at each of 10 logarithmically uniform points along the BNL normal-incident upset curve

- Simple overlayer composition:
  – Tungsten vias modeled as 0.5 µm layer 0.5 µm above the base of a 5.5 µm SiO₂ overlayer

Model calibration and results: Direct ionization events at 0° incidence

Value for critical charge (Q_{crit}) determined to be 0.133 pC for normally incident direct ionization events

- Fit the BNL data off which the model was developed
- AND high energy, directly ionizing events
Model calibration and results: 0° incidence

- Qcrit for indirect ionization events: 0.121 pC - 0.163 pC
  - Range due to simplified model geometry and Geant4 systematic errors

- Good prediction of normally-incident cross-section data
  - Directly ionizing events, threshold events showing possible Coulombic scattering contribution, and low-energy indirectly ionizing events are well-simulated
  - High-energy Argon cross-section is due to indirect ionization; limitations in Geant4 preclude accurate fit
Angle dependence

- MRED simulations reproduce 30° and 45° angle dependence for SEU response without any adjustable parameters
  - Measured data presented without angular correction to fluence
Summary

- Using MRED, we produced a reasonably accurate upset response model of a low-critical charge SRAM without detailed information about the circuit, device geometry, or fabrication process
  - The detailed physical processes included in the Geant4-based MRED tool enabled us to capture the complexities of the experimental cross-section curve
- The model was developed from low-energy, normally-incident test data
  - Angle dependence of upset response was successfully predicted from the normally-incident data
  - Simulation results suggest that for some devices and technologies, MRED may prove useful to guide limited high-energy testing for model validation and bounding in the areas of Geant4 physics limitations, possibly reducing the cost of SEU testing
Sponsors

- NASA Electronic Parts and Packaging Program
- Defense Threat Reduction Agency Radiation Hardened Microelectronics Program
- U.S. Department of Energy