

Total Dose Effects on Ge pMOSFETs with High-k Gate Stacks: On - Off Current Ratio

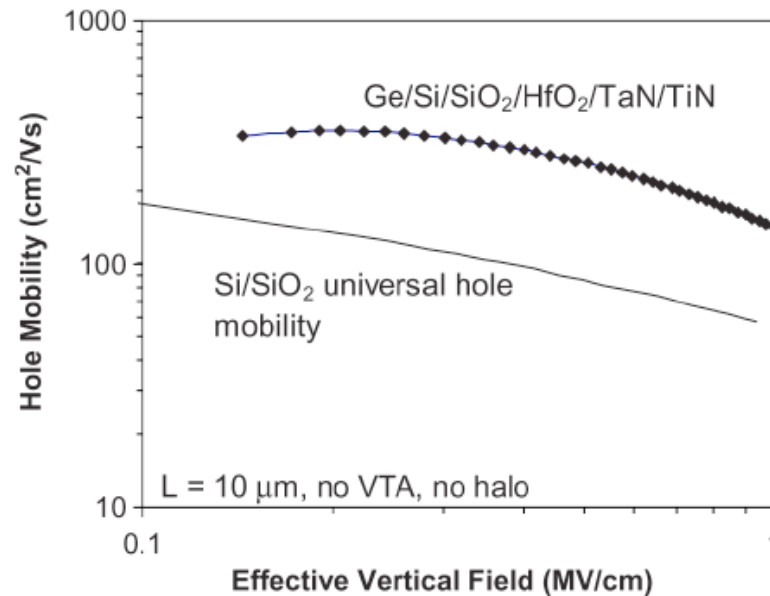
S.R. Kulkarni¹, R.D. Schrimpf¹, K.F. Galloway¹, Cor Claeys^{2,3}, and Eddy Simoen²

¹EECS, Vanderbilt University, Nashville, TN 37235, USA

²IMEC, Kapeldreef 75, B-3001 Leuven, Belgium and

³EE Department, KU Leuven, Kasteelpark Arenberg 10, B-3001, Leuven, Belgium

Motivation

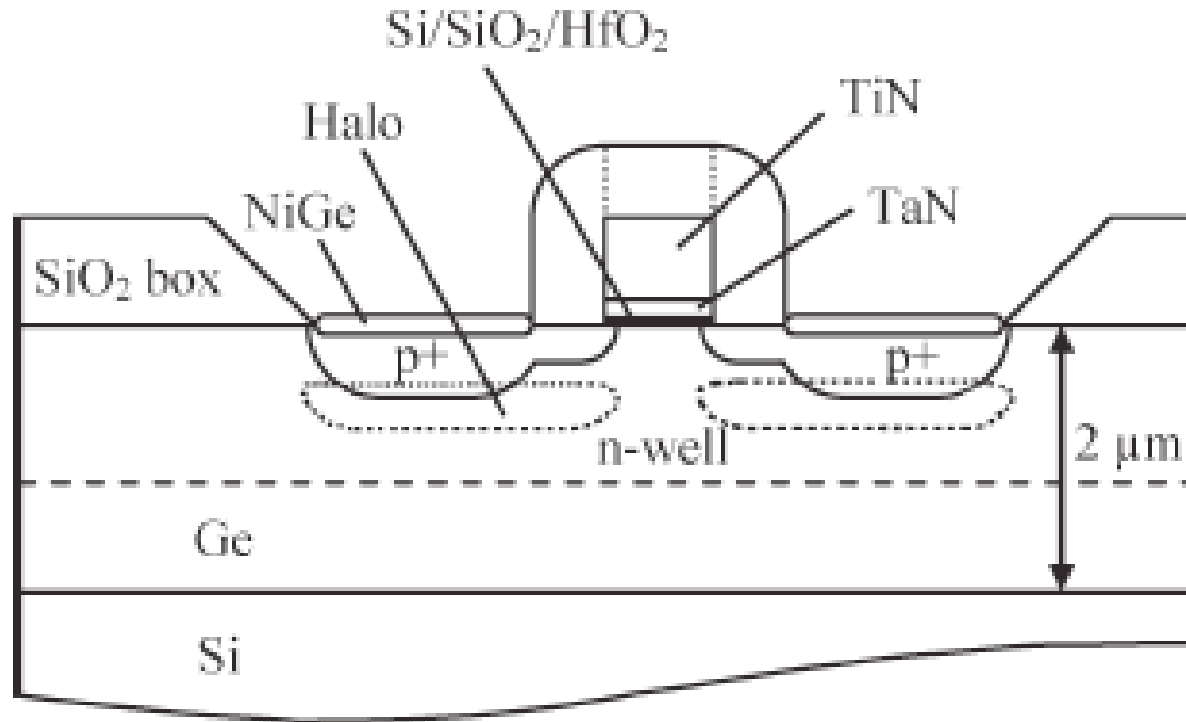


G. Nicholas, et al. IEEE Trans. Electron Devices, Vol. 54 pp 2503-2511 (2007)

To investigate the Total Ionizing Dose (TID) effects on Ge pMOSFETs with HfO₂ gate stacks

- Gate leakage current
- Change in transconductance
- Threshold voltage shift and
- **On - Off Current Ratio**

Device cross-section

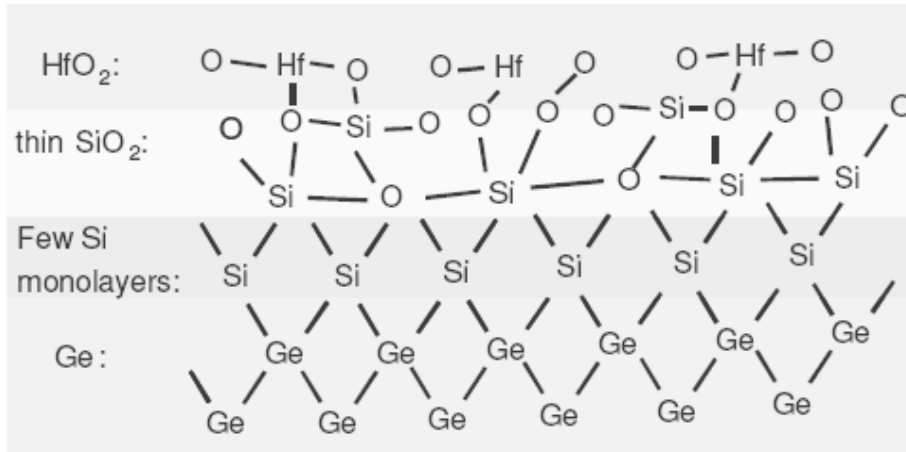
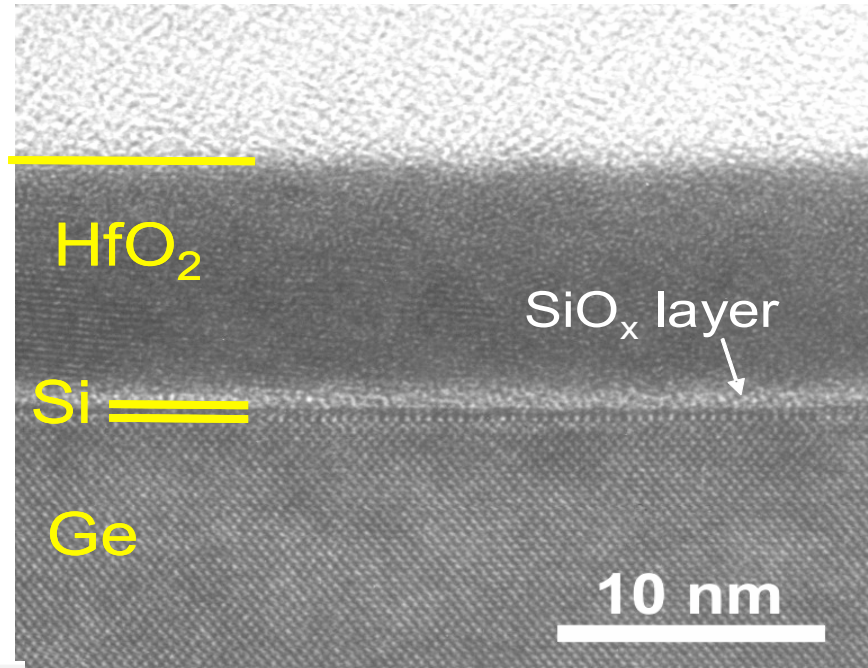


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Interface layer structure

TEM photograph of the dielectric →

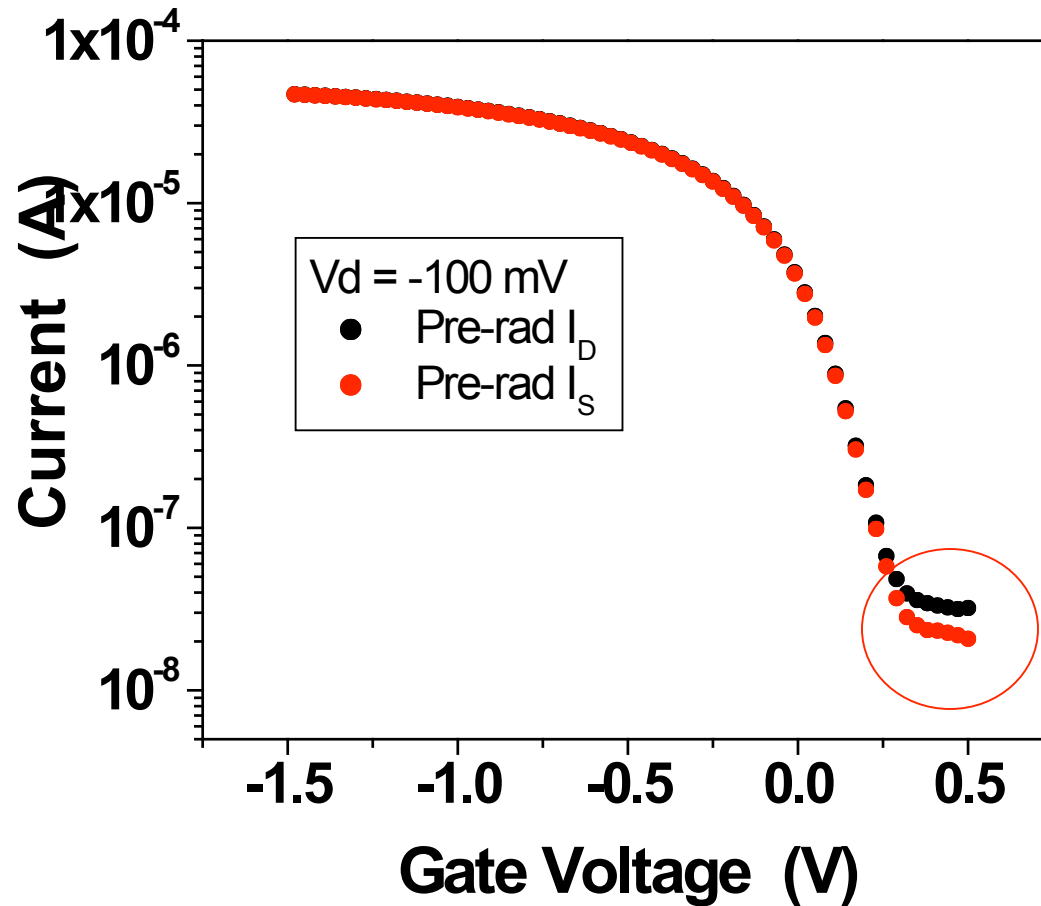
Si monolayer
 $\text{SiO}_x = 0.6 \text{ nm}$ and
 $\text{HfO}_2 = 4 \text{ nm}$
 $\text{EOT} = 1.2 \text{ nm}$



← Schematic of the final multi-layer gate dielectric stack with epitaxial Si interface layer

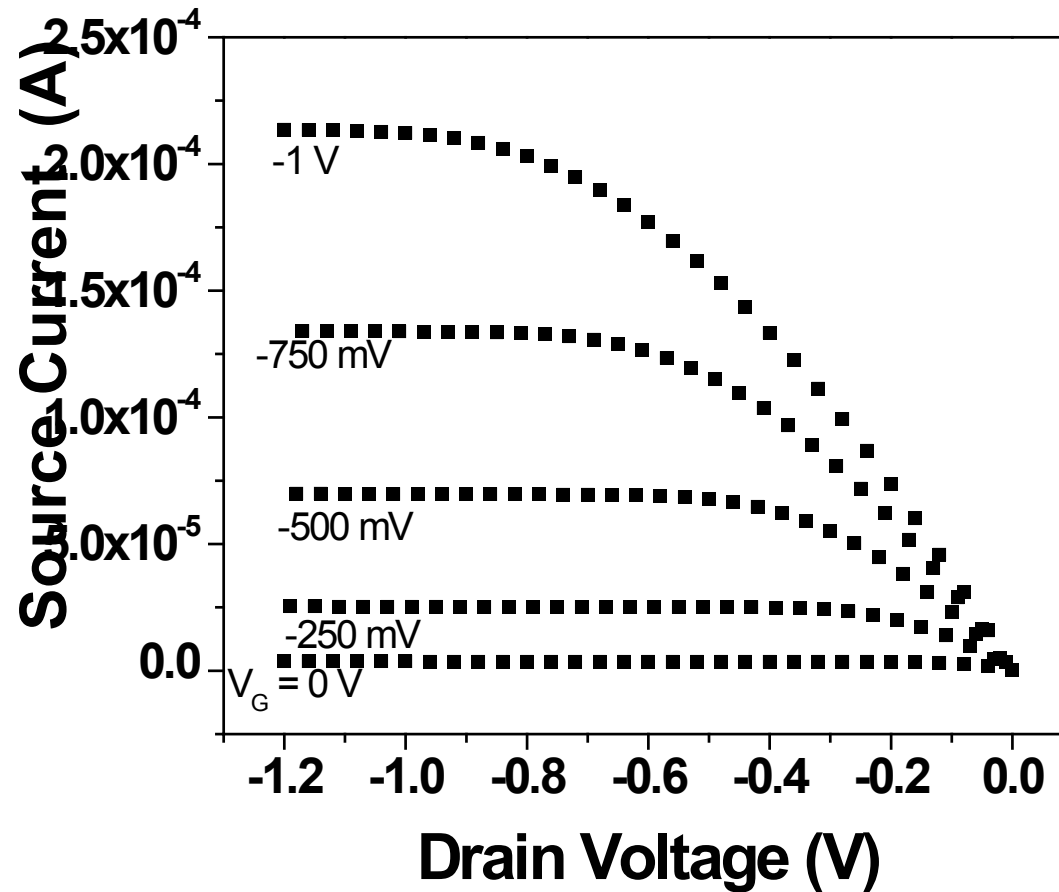
Experimental Results

Pre-rad subthreshold curves



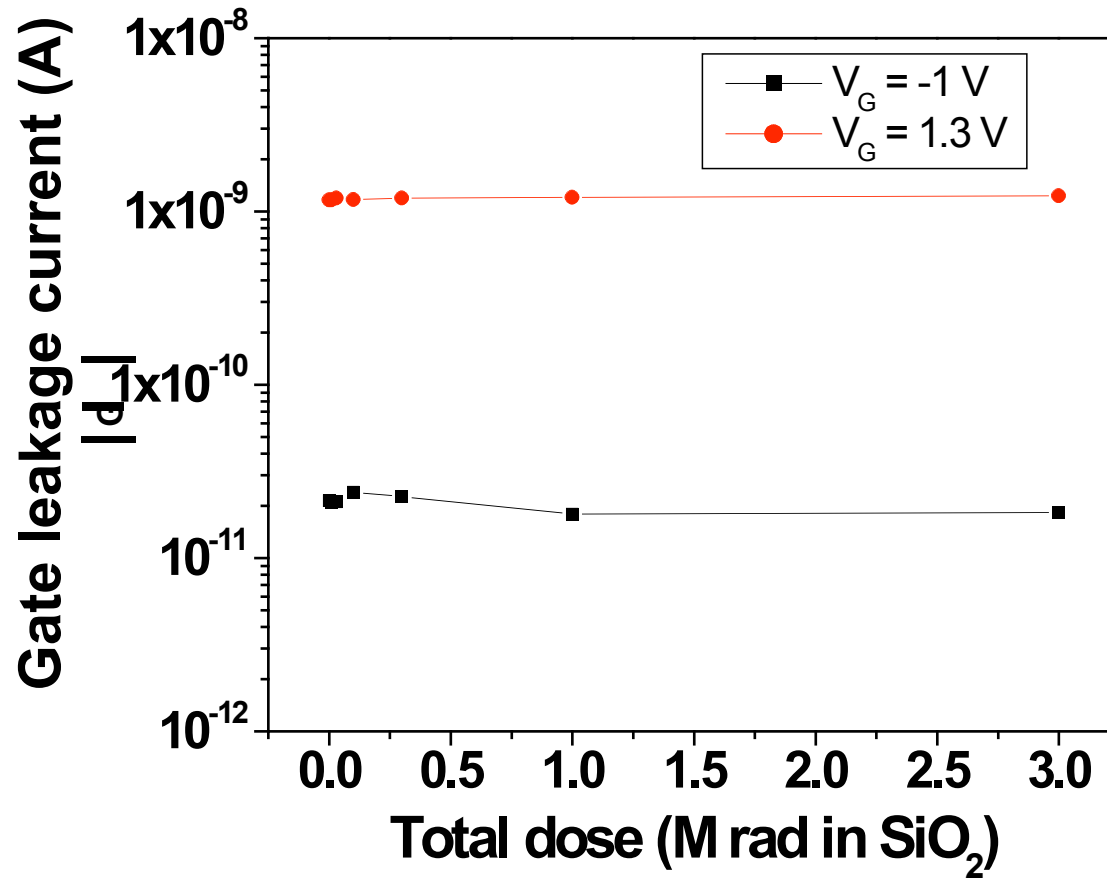
- $V_{th} = 0.16 \text{ V}$
- $SS = 130 \text{ mV/dec}$
- $I_{on}/I_{off} \sim 10^3$
- Difference in I_{off} of Drain and Source

Pre-rad $I_S - V_D$ curves



- Current measured at the source to exclude the effect of drain junction leakage

Gate leakage current for different TID

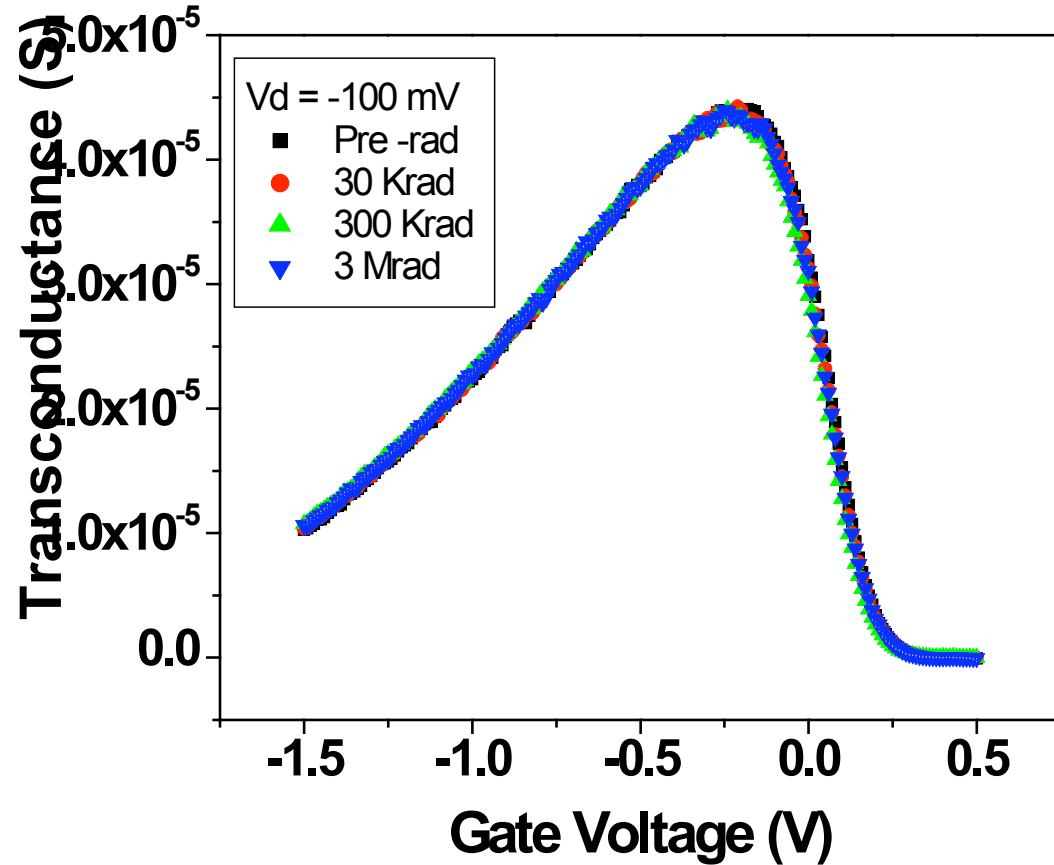


Irradiation Details

- 10 keV x-ray
- Dose rate = 31.5 krad/min in SiO₂
- V_G = 1.3 V

- No significant change in gate leakage current

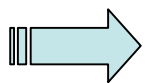
Transconductance for different TID



- No change in transconductance
 ➡ No change in channel mobility

Subthreshold curves for different TID

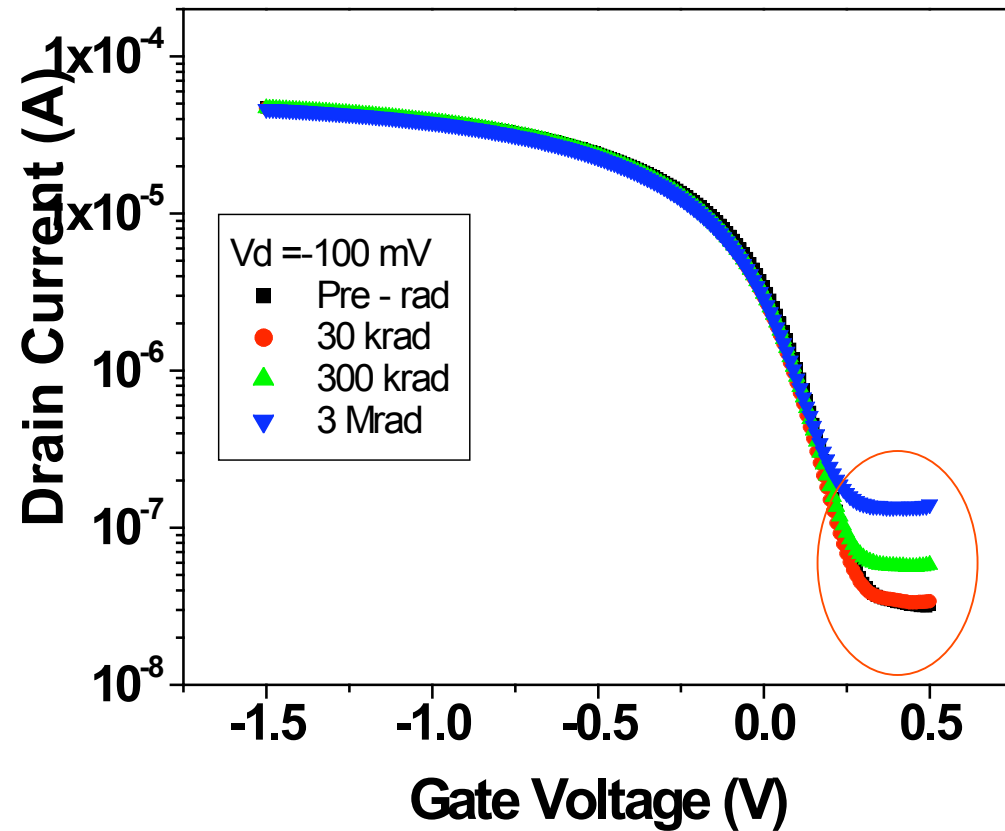
➤ No stretch-out or translation of subthreshold curves



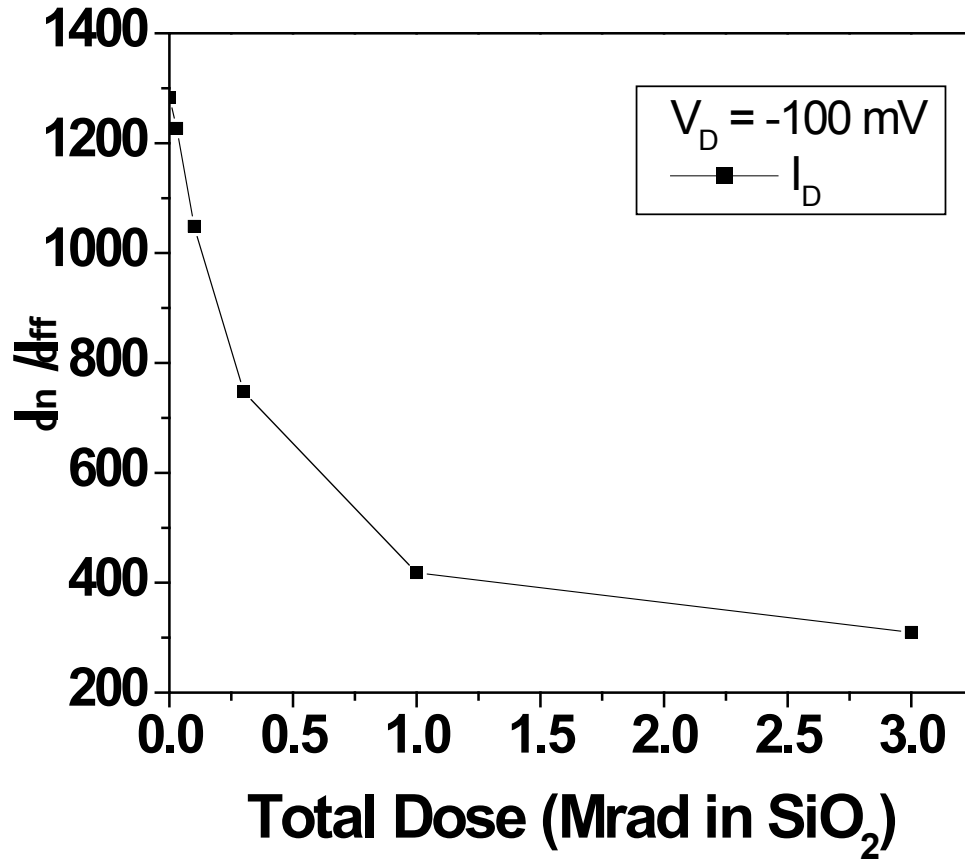
No significant charge trapping

➤ Increase in OFF-state current as a function of TID

➤ But no change in ON state current

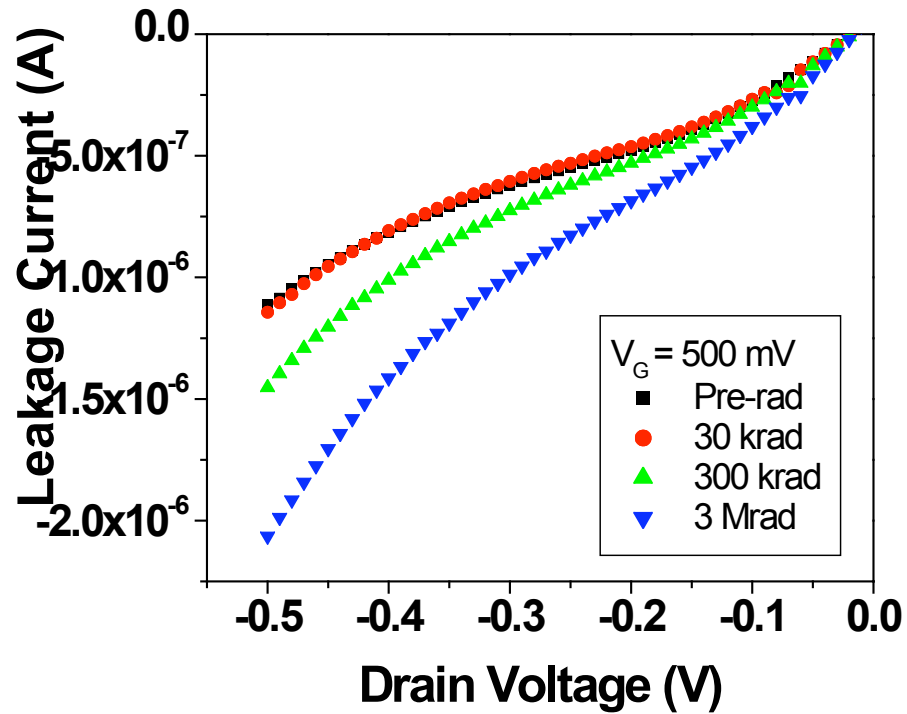


I_{on} / I_{off} ratio as a function of TID

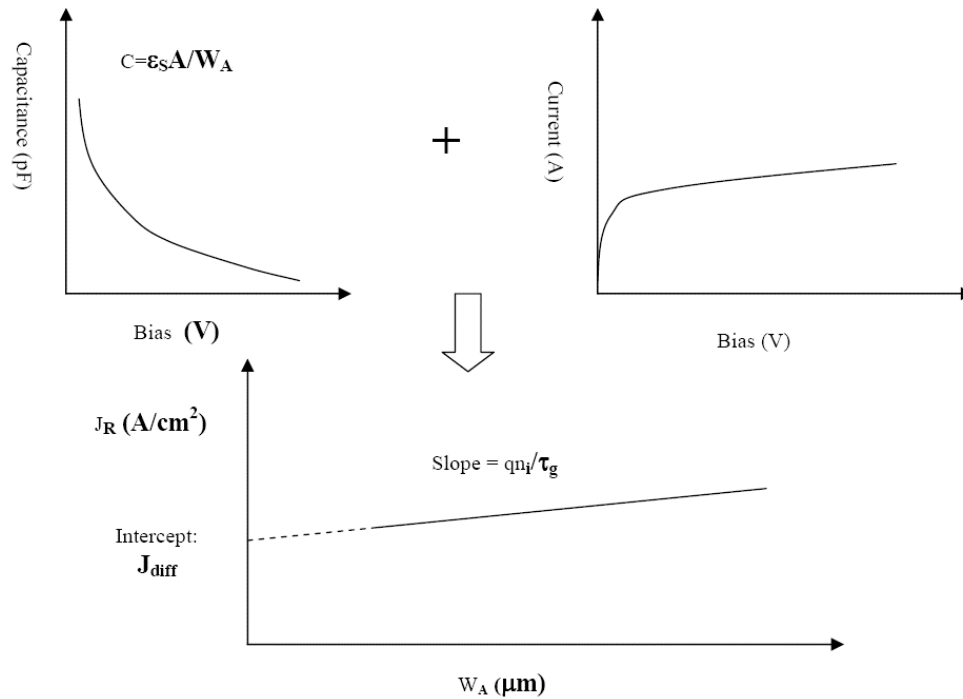


The reduction in the I_{on} / I_{off} is due to an increase in the drain to body leakage current in the device

Drain to body leakage current for different TID



$$J = J_{\text{diff}} + J_g + J_{\text{sg}}$$



Diffusion and generation parts of the leakage currents are separating using analytical method developed by Murakami and Shingyouji

Murakami and Shingyouji, J. Appl. Phys. 75(7), p 3548, 1994

Conclusions

- Threshold voltage, channel mobility and gate leakage current appear to be unchanged up to TID of 3 Mrad (SiO₂). Thus, this is a relatively hard technology for fabricating pMOSFETs
- The pre-rad I_{on}/I_{off} ratio of $\sim 10^3$ is reduced to 300 after 3 Mrad(SiO₂).
- The reduction in the I_{on}/I_{off} value or increase in the off-state current of the transistor is due to an increase in the drain to body leakage current in the device.
- The mechanism of junction leakage current increase as a function of total ionizing dose is under investigation.