



Interface Structure and Charge Trapping in HfO₂-based MOSFETS

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Radiation damage in Hafnium oxide

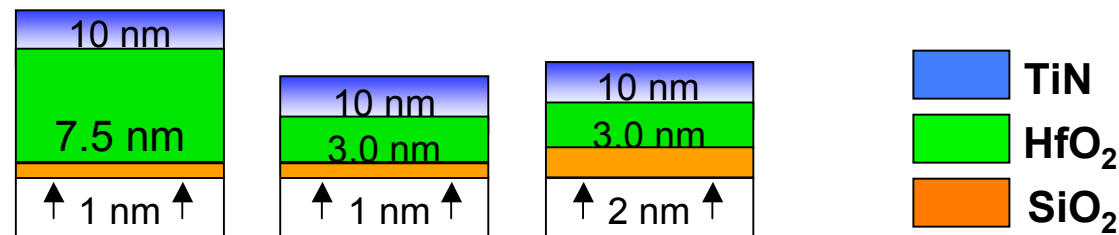
Previous radiation studies:

Most of the work on thicker oxides, mostly on capacitors

- Electron trapping reported. [Kang et al., APL, vol. 83, p. 3407, 2003](#)
- Hole trapping studied. [Felix et al., Microelectron. Engrg., vol. 44, p. 563, 2004](#), [Ryan et al., IEEE TNS, vol. 52, p. 2272, 2005](#)

This work:

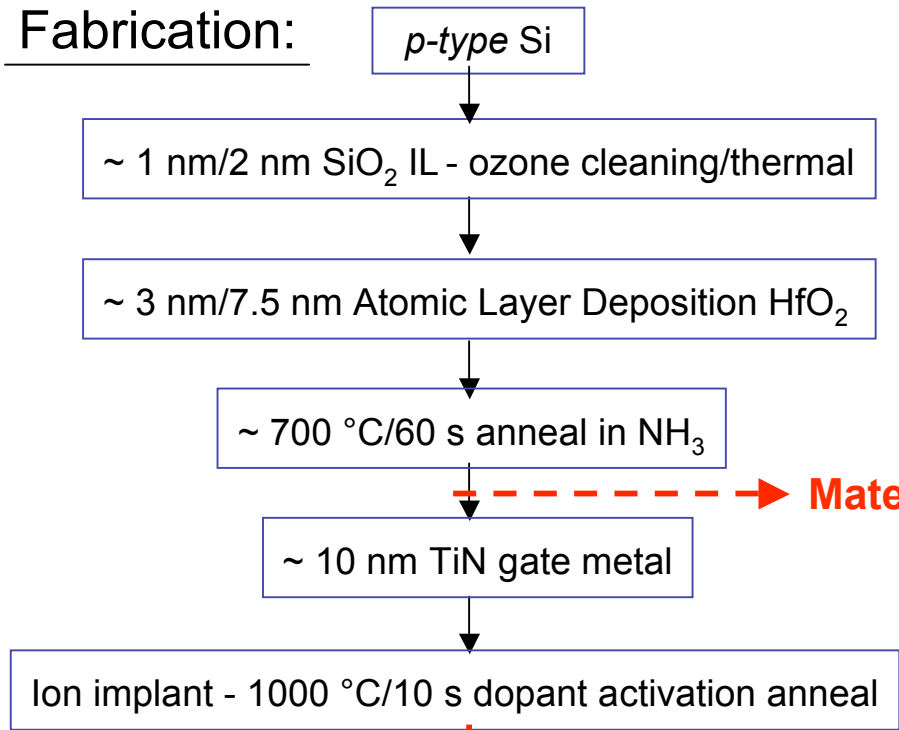
- Radiation studies on HfO₂-based MOSFETs
- Dose response of ultrathin gate oxides
- Identify the bias stress contribution for pure rad response
- Study as a function of SiO₂ IL and bulk HfO₂ thickness



- Improved trapping efficiency calculations

Device processing & irradiation

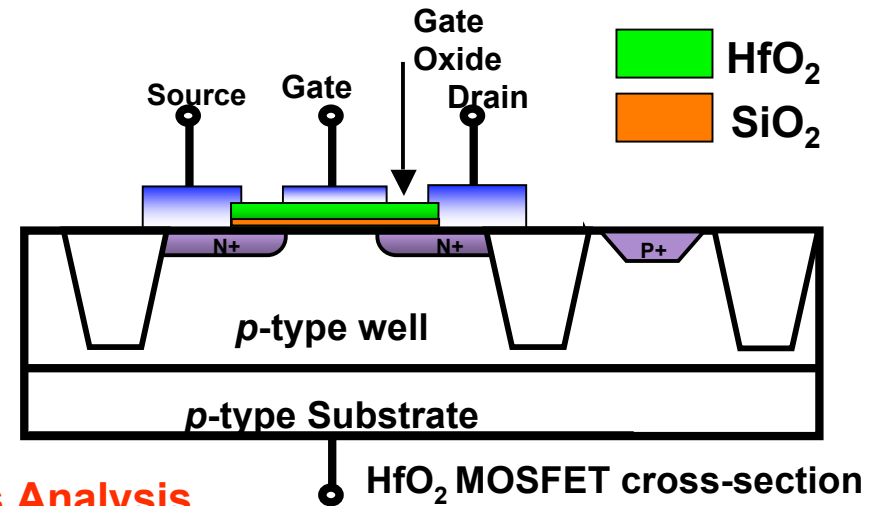
Fabrication:



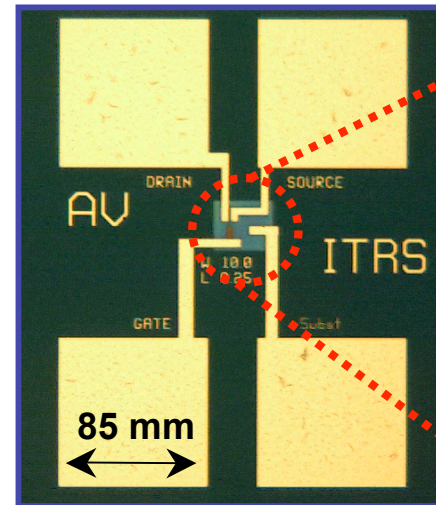
Electrical measurements

Irradiations:

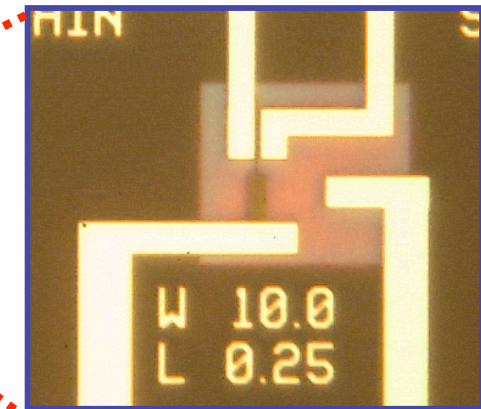
- In-situ 10 keV X-ray irradiations
- Function of bias
- Function of bulk (7.5/3 nm) & IL (1/2 nm)
- I-V characterization



Materials Analysis



AV module



High-k n-MOSFET

3

Materials and Device Characterization

Before irradiation

- Thickness of SiO_2 IL and HfO_2 bulk
- Materials composition
- Amorphous/nano-crystalline



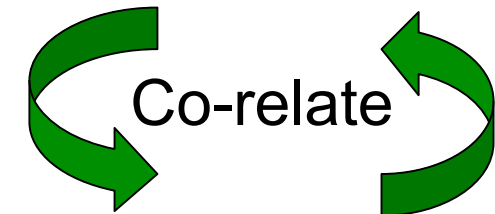
Materials
Characterization

After irradiation

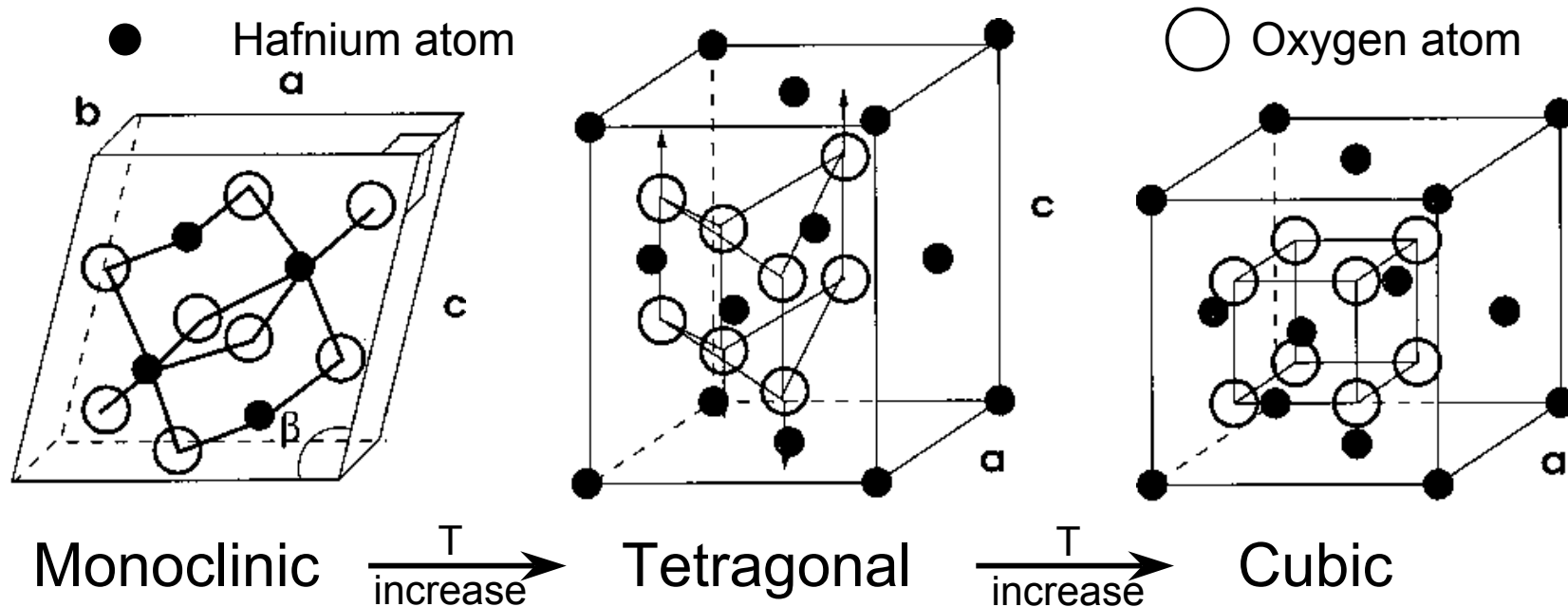
- Charge trapping (V_T shifts)
- Trap location (SiO_2 IL or HfO_2 bulk)



Electrical
Characterization



Hafnium oxide - Materials perspective



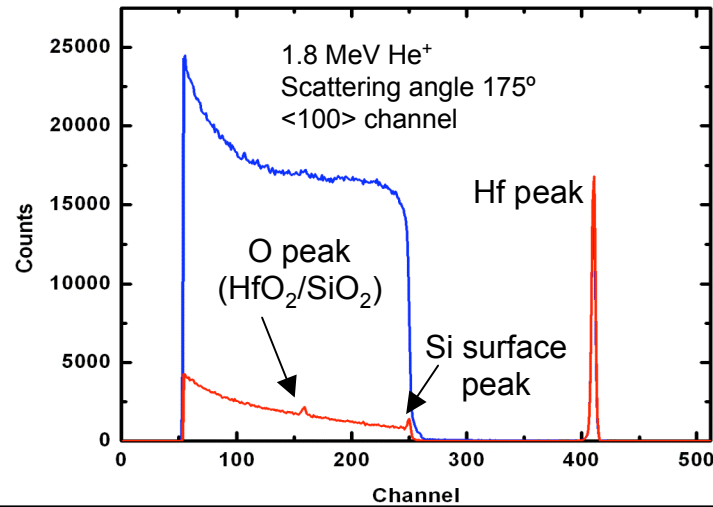
Zhao et al., PRB, vol. 65, p. 233106, 2002

- Structure - Monoclinic crystallites in amorphous matrix
- **Pure HfO₂ crystallizes** at high temp., **trapping increases with defects at GBs**
- **Alloying of Si** increases the thermal budget, κ reduced, **affects C_{ox}**
- **SiO₂ interlayer** improves mobility, **intermixing issues, amphoteric traps**

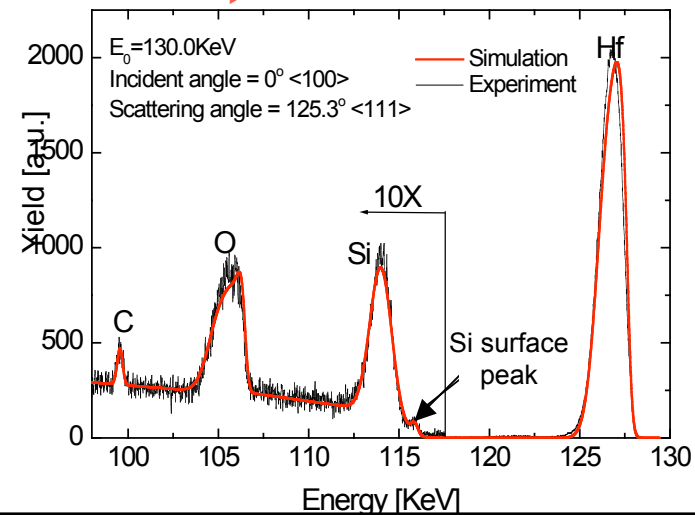
Callegari et al., JAP, v.90,p. 6466, 2001

Materials analysis - HfO₂/SiO₂ IL/Si

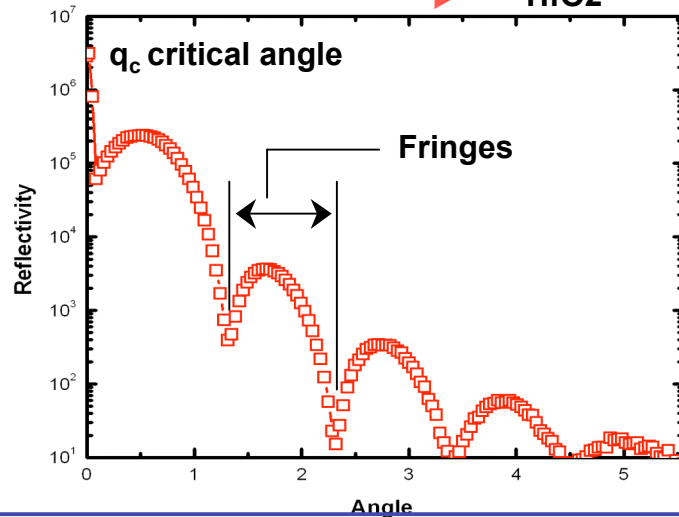
RBS/Channeling \rightarrow t_{HfO_2} & t_{SiO_2}



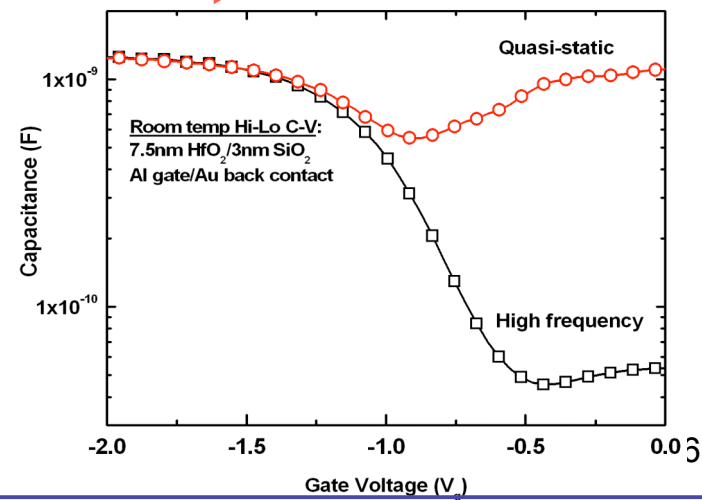
MEIS \rightarrow t_{HfO_2} , t_{SiO_2} & Si in HfO₂



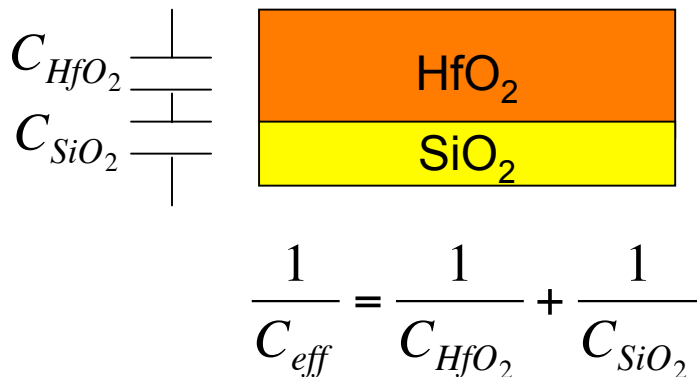
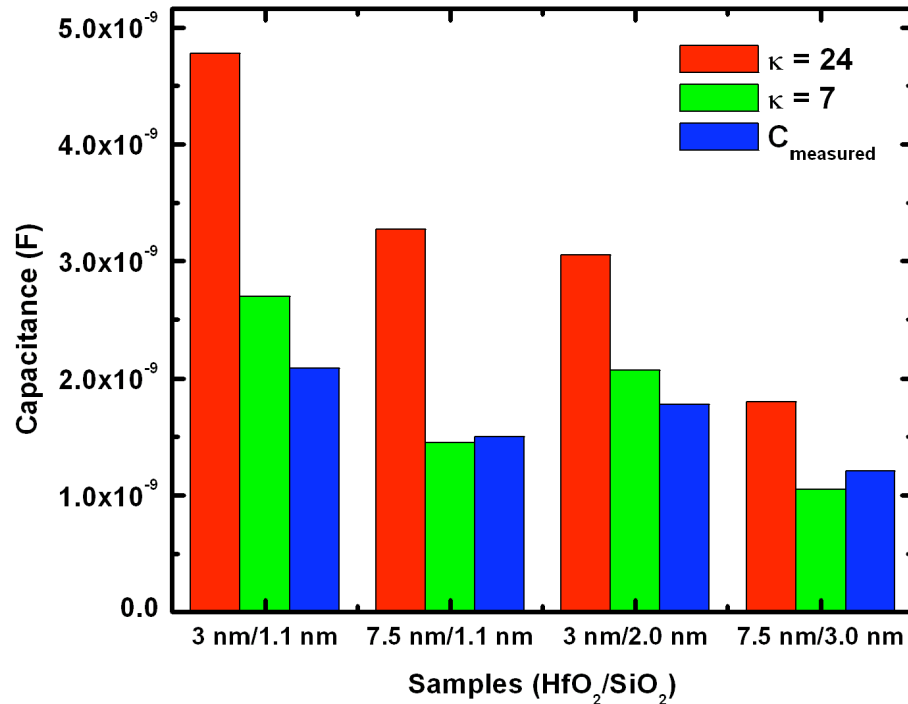
XRR \rightarrow t_{HfO_2}



C-V \rightarrow t_{HfO_2} , t_{SiO_2} & Si in HfO₂

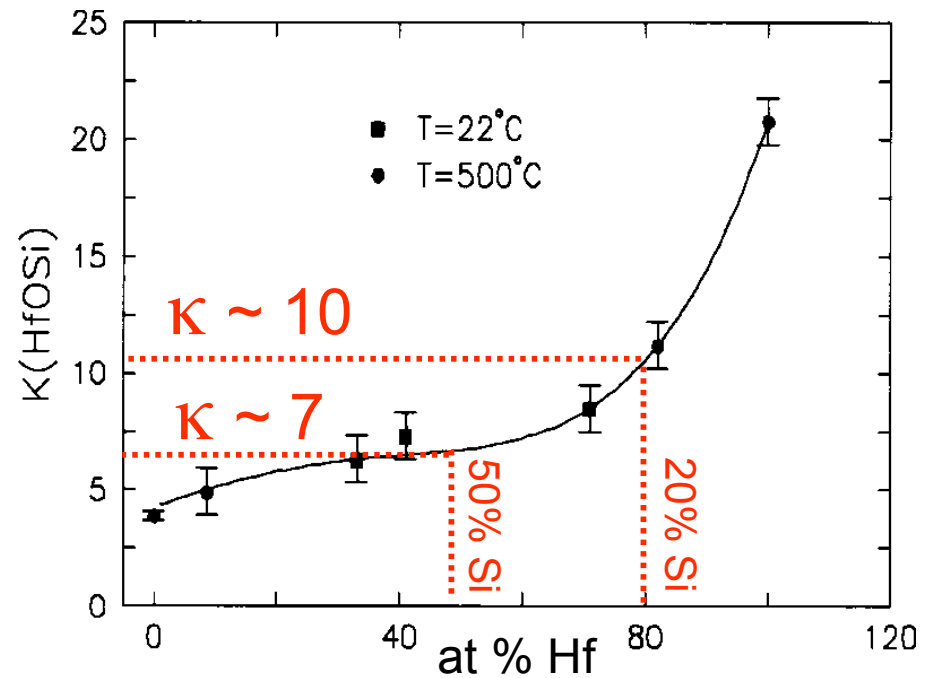


C-V - Comparison (Theory & Measured)



κ_{eff} not well-understood for silicates

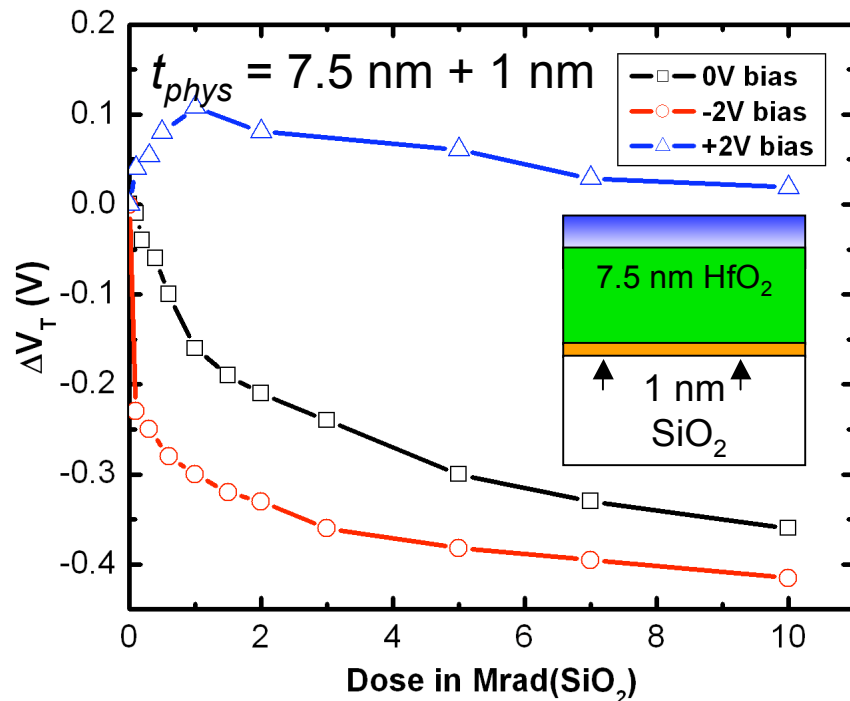
- HfO₂ deposition + PDA at 700 °C
- Intermixing issues
- Interlayer sub-stoichiometric
- κ_{eff} reduces



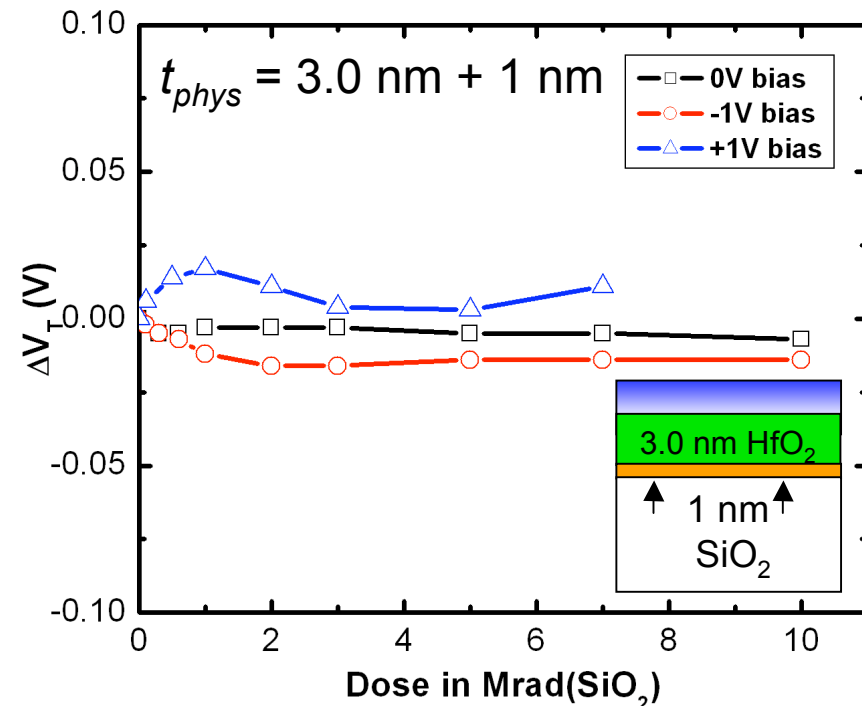
Callegari et al., JAP, v.90, p. 6466, 2001

Comparison 7.5 nm and 3 nm HfO₂ samples

Threshold voltage shifts at -2 MV/cm and +3 MV/cm gate bias



- Net hole trapping - radiation
- $\Delta N_t = \sim 3.8 \times 10^{12} \text{ cm}^{-2}$ at max.dose
- Significant SiO₂ IL trapping

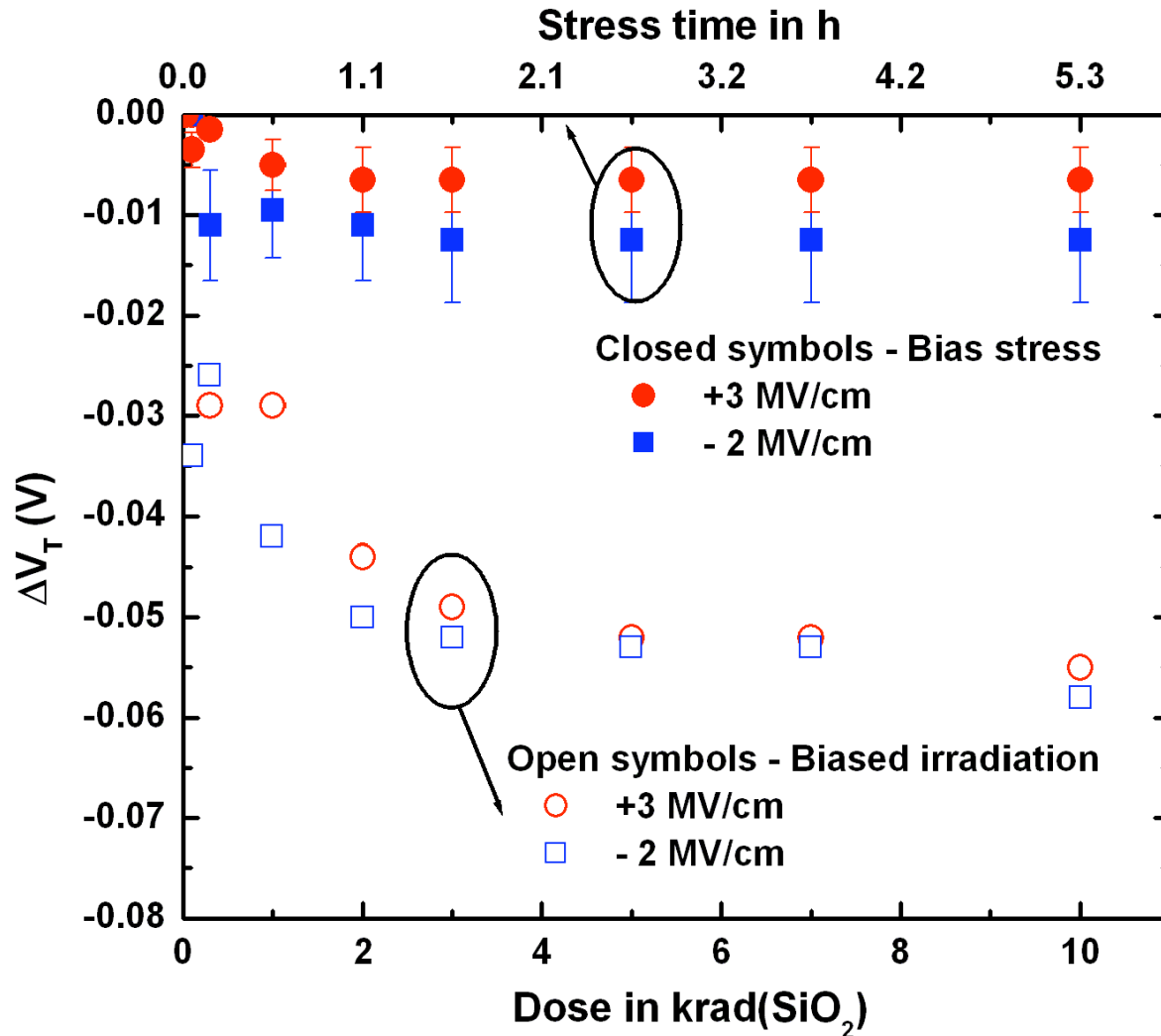


- Radiation tolerant
- $J_g \sim 10 \text{ A/cm}^2$ leakage
- No significant V_T shifts

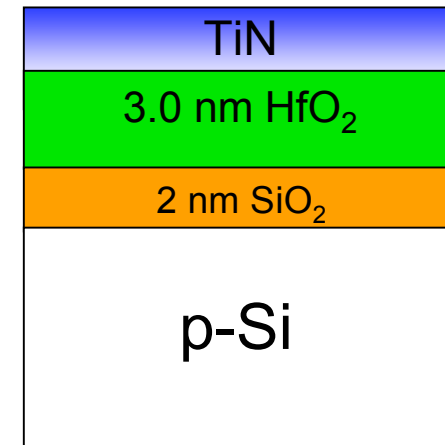
Dixit et al., IEEE TNS, vol. 54, p. 1883, 2007

Sample with minimal injection desired - pure radiation response

CVS and irradiation - 3 nm/2 nm



Dielectric stack



- Minimal shifts under bias stress
- Maximum V_T shift under irradiations was ~ 50 mV

Dixit *et al.*, manuscript to be submitted to APL, 2008

CVS and irradiation - 3 nm/2 nm

Key findings - 3 nm HfO₂/2 nm SiO₂

- IL better blocking electrode (no charge injection)
- Pure radiation response of ~ 50 mV (predominant hole trapping)
- Lowest $J_g \sim 8 \times 10^{-4}$ A/cm² (minimal neutralization of trapped charge)

Thinner HfO₂, less bulk traps
Felix *et al.*, Ryan *et al.*, & Foster *et al.*

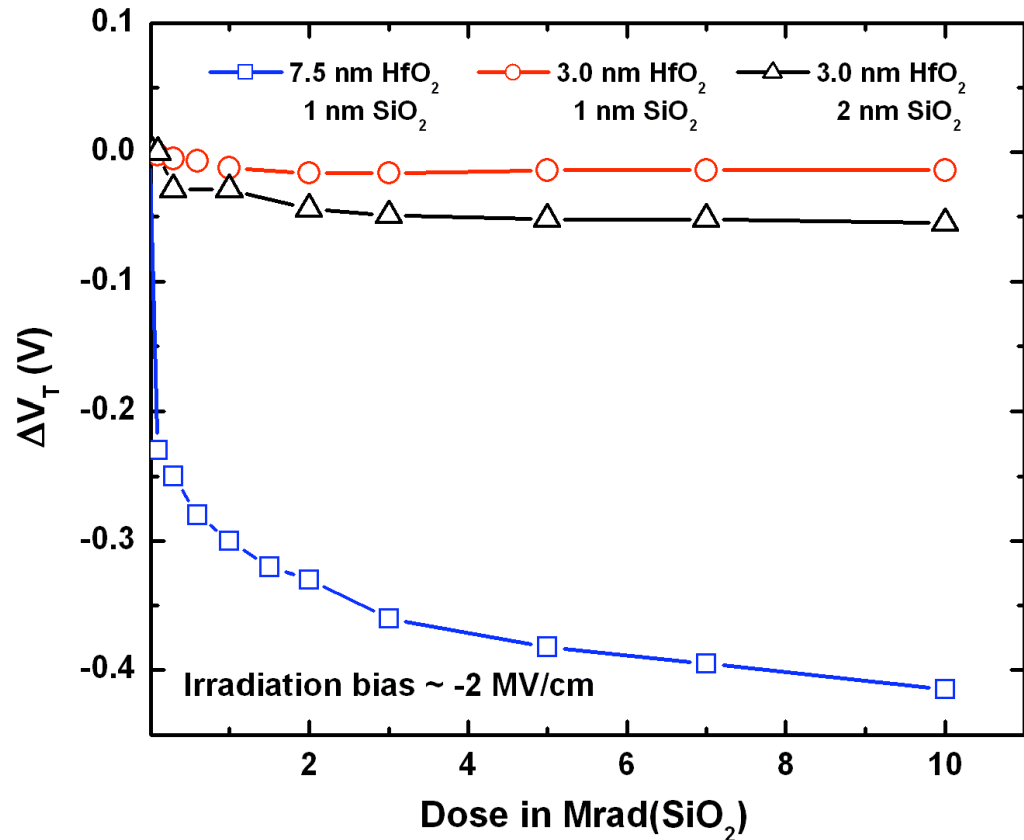
Felix *et al.*, *Microelectron. Engrg.*, vol. 44, p. 563, 2004
Ryan *et al.*, *IEEE TNS*, vol. 52, p. 2272, 2005
Foster *et al.*, *PRB*, vol. 65, p. 174117, 2002.



Thinner HfO₂, better SiO₂ IL
Bersuker *et al.* & Ryan *et al.*,

Bersuker *et al.*, *JAP*, vol. 100, p. 094108, 2006,
Ryan *et al.*, *APL*, vol. 90, p. 173513, 2007

Total dose results comparison



Irradiation Bias ~ -2 MV/cm

Key results 3 nm HfO₂/2 nm SiO₂

- IL O leaching ↑ 7.5 nm HfO₂
(exposure t ↑ at higher temp. growth^{a,b})
- I-V sweeps modify the charge (~ 50%)
(border traps in the SiO₂ IL^c)
- Residual V_T after stabilization
(traps in HfO₂ and/or away from interface)

^aBersuker *et al.*, JAP, vol. 100, p. 094108, 2006,

^bRyan *et al.*, APL, vol. 90, p. 173513, 2007,

^cFleetwood *et al.*, IEEE TNS, vol. 39, p. 269, 1992.

Conclusions - HfO₂ based MOSFETs

- 3 nm/1 nm devices radiation tolerant and resistant to constant-voltage stress
- Total dose comparison between 7.5 nm/1 nm and 3 nm/2 nm MOSFETs suggest substantial hole trapping in the SiO₂ IL
- Residual V_T shift suggest the presence of some of the holes trapped charge away from the interface, probably in the HfO₂ bulk

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Publications

Published work

- 1) **S. K. Dixit**, X. J. Zhou, R. D. Schrimpf, D. M. Fleetwood, S. T. Pantelides, R. Choi, G. Bersuker and L. C. Feldman, “Radiation induced charge trapping in ultrathin HfO₂-based MOSFETs,” *IEEE Trans. Nucl. Sci.*, vol. 54, pp. 1883-1890, 2007.

Manuscripts in preparation

- 2) **S. K. Dixit**, E. Garfunkel, C. D. Young, G. Bersuker, and L. C. Feldman, “Physical and electrical characterization for bulk HfO₂ and SiO₂ interlayer (IL) thickness verification in advanced gate stacks,” manuscript to be submitted to *J. Appl. Phys.*
- 3) **S. K. Dixit**, X. J. Zhou, R. D. Schrimpf, D. M. Fleetwood, C. D. Young, G. Bersuker and L. C. Feldman, “Hole trapping in HfO₂-based MOSFETs studied as a function of varying HfO₂ bulk and SiO₂ interlayer (IL) thicknesses,” manuscript to be submitted to *Appl. Phys. Lett.*